

Serial No.: 10756,961

Examiner: H. Pham

Title: SEMICONDUCTOR MEMORY DEVICE WITH TRENCH-TYPE STACKED CELL CAPACITORS AND
METHOD FOR MANUFACTURING THE SAME

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REMARKS/ARGUMENTS

Reconsideration is requested in view of the following remarks. Claims 7, 8, 16, 17 and 21 remain pending in the application.

Claim Rejections – 35 USC §103

Claims 7-8, 16-17 and 21 are rejected under 35 U.S.C. §103(a) as unpatentable over Huang (US 6,617,631) in view of Lee et al. (US 6,381,165). Applicant respectfully traverses this rejection.

A verified translation of application Serial No. JP2001-033445, filed on February 9, 2001 in Japan, for which Applicant has claimed priority under 35 U.S.C. §119, is attached herewith. Since application Serial No. JP2001-033445 has a priority date before the effective prior art date of Lee et al. (September 28, 2001), Lee cannot be used as a prior art reference against Applicant's claimed invention.

For at least these reasons, claim 7 is patentable over the cited references. Claims 8, 16-17 and 21 are also patentable over the cited references since these claims depend from claim 7 that is allowable.

Favorable reconsideration in the form of a Notice of Allowance is requested. If the Examiner believes a telephone conference would advance the prosecution of this application, the Examiner is invited to telephone Applicant's primary attorney-of-record, Douglas P. Mueller (Reg. No. 30,300), at (612) 455-3804.

Dated: June 1, 2006

Respectfully submitted,

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